



Leader in Patent Analysis and Transaction Advisory Services

High-Performance Computing Portfolio

Patent Acquisition Opportunity

The information in this package is provided for the sole purpose of supporting the independent evaluation of these patents by prospective buyers, and does not constitute legal opinion or representation. Any discussion of the potential use of the patented inventions by particular products is for illustration purposes only and is not intended to be, nor should they be construed as, notice of, or any form of accusation of, infringement of any of the indicated patents by any products. In addition, the materials provided by GTT Group are copyrighted material and are intended for use by the receiving party solely for their use in determining whether to purchase the patents offered for sale by GTT Group and their participation in the sales process run by GTT Group.

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High-Performance Computing Portfolio



Patents

4 US Patents

1 US Application

6 global counterparts

Relevant Markets

High-Performance Computing

High-Bandwidth Memory

GPU-Based Systems

Servers

Making an Offer

Interested parties should make an offer as soon as they are able. Offers will be reviewed as they are received.

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Opportunity Summary

- GTT Group is exclusively representing the sale of the High-Performance Computing Portfolio.
- The portfolio contains 11 global assets in one patent family, including four issued US patents and one pending US application.
 - Other covered jurisdictions are China, EP, Japan, Singapore and WO.
- The patented technology provides a high bandwidth, low latency, modular interface between a CPU and associated peripheral circuitry such as high speed communication ports or memory channels.
- A few of the applications of the technology are:
 - High-performance computing systems,
 - Embedded processors, and
 - Servers
- GTT Group has developed evidence of use (EOU) materials that are available under NDA for qualified purchasers. Please contact GTT Group for further information.

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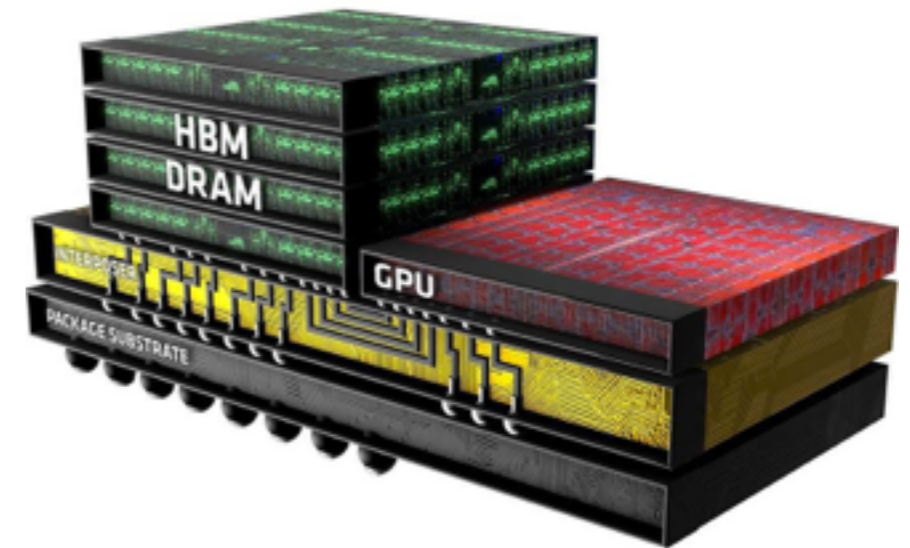
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Interposers in High-Performance Computing

- Interposers are an electrical interface used to spread connections to a wider pitch or to reroute connections.
- With an interposer, chips can be stacked side-by-side, which enables dice to be put next to each other in a high-bandwidth, low-latency configuration.
- An interposer may be used in a multi-chip module (MCM) to provide the interconnect between the various chips in the module. The MCM may then be mounted to a circuit substrate to provide additional circuitry, such as power supply circuitry, to provide additional mechanical strength, or inserted between a CPU and its socket on the main board to add low-latency “L3” cache to a processor.
- Interposers are being used in high-bandwidth memory (HBM) stacks to fully realize performance.
- HBM applications already include graphics, networking and high-performance computing. The technology is also likely to be implemented in applications that require low power and high bandwidth (e.g. artificial intelligence, autonomous vehicles, deep learning, etc.)



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Transaction Structure

Seller prefers an all-cash transaction.

Bidding

Interested parties are encouraged to make an offer as soon as they are able. Offers will be reviewed and responded to promptly. Priority will be given to offers received first.

Submitting an Offer

Offers should be submitted in writing from an authorized party stating the price offered and any material condition(s) to closing.

Pricing

No price floor has been set. Seller reserves the right to accept any offer and refuse any and all offers in its sole and arbitrary discretion. GTT Group is able to provide value guidance to qualified interested parties.

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Non-Notice

Seller has agreed that the information provided to prospective purchasers throughout this sales campaign will not be used in any legal or administrative proceeding directed to enforcement of any patent associated with the High-Performance Computing Portfolio.

Encumbrances

No encumbrances exist for the High-Performance Computing Portfolio.